

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED TR4 O.A. AG	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/93.1104	F	257	48	2811 2826	FM-2826

NTS Ei ji Yoshida

Semiconductor device and method of analyzing same

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner) _____ (Date) _____				
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				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed				ISSUE BATCH NUMBER	
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